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## APPLICANTS

Yoshinobu Kimura, Tokyo, JAPAN;

 Takahiro Kamo, Mobara, JAPAN;  
 Yoshiyuki Kaneko, Hachioji, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP00/04141 06/23/2000

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 11/181559 06/28/1999

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 16	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 9
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged  Examiner's Signature: Initials:				

## ADDRESS

 020457  
 ANTONELLI, TERRY, STOUT & KRAUS, LLP  
 1300 NORTH SEVENTEENTH STREET  
 SUITE 1800  
 ARLINGTON, VA  
 22209-3873

## TITLE

Polysilicon semiconductor thin film substrate, method for producing the same, semiconductor device, and electronic device

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